



U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0036		
				Application No.: 10/675,529		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Jerrell Hein		
(Use several sheets if necessary)				Filing Date: September 30, 2003		
				Group Art Unit: 2182		
				Date Submitted: October 4, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
RBF	AA	3,676,801	Jul. 11, 1972	Musa		
	AB	3,719,838	Mar. 6, 1973	Peduto et al.		
	AC	3,887,941	Jun. 3, 1975	Dann et al.		
	AD	4,038,612	Jul. 26, 1977	Borofka et al.		
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	AF	4,380,745	Apr. 19, 1983	Barlow et al.		
	AG	4,446,446	May 1, 1984	Fowks		
	AH	4,485,818	Dec. 4, 1984	Leckrone et al.		
	AI	4,580,107	Apr. 1, 1986	Caldwell et al.		
	AJ	4,598,257	Jul. 1, 1986	Southard		
	AK	4,683,445	Jul. 28, 1987	Erickson		
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		Document	Date	Country	Yes	No
RBF	AL	0 239 412 A2	Sep. 30, 1987	Europe		
	AM	0 393 975 A1	Oct. 24, 1990	Europe		
	AN	0 402 736 A2	Dec. 19, 1990	Europe		
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RBF	AP	Cypress Semiconductor Corporation, "Cypress 1 PLL In-System Programmable Clock Generator with Individual 16K EEPROM," December 14, 2002, 18 pages.				
	AQ	Encinas, J. B., "Phase Locked Loops," Chapman & Hall, 1993, pages 144-158.				
	AR	Foord, A., "Voltage Controlled Oscillator," Radio & Electronics Constructor, May 1975, pp. 590-595.				
	AS	Fox Electronics, "1995-1996 Frequency Control Products Catalog," Fort Meyers, Florida, 1995, 72 pages.				
Examiner - <i>Antonia B. Chumbley</i>		Date Considered 2/22/2006				
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RBF	AT	4,689,581	Aug. 25, 1987	Talbot		
	AU	4,827,226	May 2, 1989	Connell		
	AV	4,847,888	Jul. 11, 1989	Cox et al.		
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RBF	AEE	WO 85/ 02966	Jul. 4, 1985	PCT		
	AFF	02-291161 (English abstract)	Nov. 30, 1990	Japan		X
	AGG	03-297223 (English abstract)	Dec. 27, 1991	Japan		X
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	AII	Fox Electronics, "F6151 / Tri-State HCMOS Dual Programmable Clock Oscillator," 4 pages (from file history of U.S. Patent No. 5,952,890).				
	AJJ	Fox Electronics, "F6053A / Tri-State HCMOS Programmable Clock Oscillator," 4 pages (from file history of U.S. Patent No. 5,952,890).				
	AKK	Fox Electronics, "F6233 Quick Delivery Oscillator," 1 page (from file history of U.S. Patent No. 5,952,890).				
Examiner -		Date Considered				
Richard B. Greenhalgh		2/22/2006				
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RBF	AWW	Gardner, Floyd M., "Phaselock Techniques," Second Edition, John Wiley & Sons, 1979, pages 2-5.	
	AXX	Gaskell, Tom, "Semiconductor Circuits Programmable Crystal Oscillator (PX0-600)," <u>Practical Electronics</u> , July 1983, pages 40-43.	
	YYY	Giles, T. G., "A Universal Frequency Synthesizer IC," <u>Philips Telecommunication Review</u> , Vol. 37, No. 3, August 1979, pages 177-181.	
	AZZ	Goto, Junichi, et al., "A Programmable Clock Generator With 50 to 350 MHz Lock Range For Video Signal Processors," <u>IEEE 1993 Custom Integrated Circuits Conference</u> , pages 4.4.1 - 4.4.2.	
	AAAA	"IC Designs Introduces Trio of Parts Aimed at Replacing Crystal Oscillators in Personal Computer Graphics Board Designs," <u>News Release</u> , May 9, 1990, 1 page.	
	ABBB	Hybrids International, Ltd., "Hybrids International Product Summary," March 1993, Olathe, Kansas, 7 pages.	
	ACCC	IC Designs, "Satellite Oscillator," Model No. ICD2031A Preliminary Data Sheet, V2.0, May 1991, pages 1-8.	
	ADDD	IC Designs, "Frequency Multiplier," Model No. ICD2032 Preliminary Data Sheet, V2.0, May 1991, pages 1-7.	
	AEEE	IC Designs, "Dual VGA Clock Generator," Model No. ICD2042A, prior to 1995, 1 page.	
Examiner <i>Richard B. Schuchter</i>		Date Considered <i>2/22/2006</i>	
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	AGGG	5,416,443	May 16, 1995	Cranford, Jr. et al.
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	AIII	5,428,319	Jun. 27, 1995	Marvin et al.
	AJJJ	5,448,204	Sep. 5, 1995	Gardner
	AKKK	5,451,912	Sep. 19, 1995	Torode
	ALLL	5,471,661	Nov. 28, 1995	Atkinson
	AMMM	5,473,289	Dec. 5, 1995	Ishizaki et al.
	ANNN	5,473,758	Dec. 5, 1995	Allen et al.
	AOOO	5,952,890	Sep. 14, 1999	Fallisgaard et al.
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	AQQQ	IC Designs, "Programmable Clock Oscillator," Model No. ICD2053A Programmable Products, V1.0, prior to 1995, pages 14 pages.		
	ARRR	IC Designs, "Graphics Frequency Synthesizers," Model No. ICD2061A, prior to 1995, 1 page.		
	ASSS	IC Designs, "Dual Programmable ECL/TTL Clock Generator," Model No. ICD2062B, prior to 1995, 1 page.		
	ATTT	IC Designs, "Programmable Graphics Clock Generator," Model No. ICD 2063, prior to 1995, 1 page.		
	AUUU	IC Designs, Model No. ICD 6233, Specifications, February 11, 1994, 32 pages		
	AVVV	IC Designs, "Application Note: Power Feed and Board Layout Issues," May 1991, 3 pages.		
	AWWW	Lovelock, Peter A., "Build This Programmable Crystal-Controlled Pulse Generator," Radio Electronics, June 1990, pages 47-50.		
	AXXX	MF Electronics Corp., "Crystal Controlled Oscillators 20 to 220 MHz Programmable ECL," prior to 1995, pages 9, 11, 18-20.		
	AYYY	<del>Mielke, Erhard, "XP 002061415 Programmierbarer 50-MHz-Multitaktimpuls-generator," 2421 Radio Fernsehen Elektronik 39 (1990) No. 9, Berlin, 4 pages</del>		
Examiner <i>Richard B. Chambers</i>		Date Considered <i>2/22/2006</i>		
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	AAAAA	6,456,164	Sep. 24, 2002	Fan	
	ABBBB	6,570,453	May 27, 2003	Su et al.	
	ACCCC	5,604,468	Feb. 18, 1997	Gillig	
<b>Foreign Patent Documents</b>					
					<b>Translation</b>
		Document	Date	Country	Yes      No
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	AEEEE	Wenzel Associates, "Custom Oscillator Configurations," Austin, Texas, 1 page (from file history of U.S. Patent No. 5,952,890)			
	AFFFF	M-Tron, "MT1135 Series Dual Baud Rate Generators," 1987, 3 pages.			
	AGGGG	M-Tron, "MV Series VCXO Oscillators," prior to 1995, 1 page			
	AHHHH	National Semiconductor, "PLL™ Low Power Frequency Synthesizer for RF Personal Communications," Advance Information, January 1998, 18 pages.			
	AIIII	Soderquist, Donn, "Digitally Programmed Oscillator is Suitable for μP Control," Precision Monolithics Inc., June 21, 1997, pp. 102, 104.			
	AJJJJ	Ooms, William J., "Improved Frequency Synthesizer," 8182 Motorola Technical Disclosure Bulletin, October 1986, Schaumbourg, ILL, 3 pages.			
	AKKKK	Vectron Laboratories, "Sonet Clock Recovery Module 622.08 MHz," Model No. SCRM-622, 1994, 4 pages.			
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